

**PATENT**

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE  
PATENT APPLICATION**

Application No.: 09/682,131  
 Applicant: Fujio et al.  
 Filing Date: July 25, 2001  
 Title: SEMICONDUCTOR INTEGRATED  
 CIRCUIT DEVICE WITH EMI  
 PREVENTION STRUCTURE

Atty. Docket: JP9-2000-0229  
 Today's Date: March 3, 2004  
 Examiner: Chris C. Chu  
 Group Art Unit: 2815  
 Fax: 703-872-9306

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**Amendments Under 37 CFR 1.114**

**Amendment E**

**OFFICIAL**

Honorable Commissioner of Patents and Trademarks

Washington, D.C. 20231

Sir:

In response to the Office Action dated December 9, 2003, Applicants respectfully request reconsideration of the outstanding rejections and reexamination of the present application in light of the following remarks and amendments.

**CERTIFICATE OF MAILING**

I hereby certify that, on the date shown below, this correspondence is being:

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Date: 3/3/2004

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Annette Dodge  
Name

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Signature

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09/682,131

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